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Sheet

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Complete if Known

Application Number	10/057,707
Filing Date	January 24, 2002
First Named Inventor	Zhang, Pengfei
Group Art Unit	2817
Examiner Name	Unassigned

Attorney Docket Number 020798-000710US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
MBS	AA	4,612,585		Takase et al.	09-16-1986	
	AB	4,766,497		Banach et al.	08-23-1988	
	AC	5,635,892		Ashby et al.	06-03-1997	
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MBS	AG	6,031,432		Schreuders	02-29-2000	
	AH					

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MBS	AI	LAIVI et al., "A 2.6-GHz/5.2-GHz Frequency Synthesizer in 0.4-μm CMOS Technology", IEEE Journal of Solid-State Circuits, vol. 35, no. 5, May 2000, pp. 788-794	
	AJ	RATEGH et al., "A CMOS Frequency Synthesizer with an Injection-Locked Frequency Divider for a 5-GHz Wireless LAN Receiver", IEEE Journal of Solid-State Circuits, vol. 35, no. 5, May 2000, pp. 780-787	
	AK	SAMAVATI et al., "A 5-GHz CMOS Wireless LAN Receiver Front End", IEEE Journal of Solid-State Circuits, vol. 35, no. 5, May 2000, pp. 765-772	
	AL	WONG et al., "A Wide Turning Range Gated Varactor", IEEE Journal of Solid-State Circuits, vol. 35, no. 5, May 2000, pp. 773-779	
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	AN	BAUTISTA et al., "A High IIP2 Downconversion Mixer Using Dynamic Matching", "IEEE Journal of Solid-State Circuits, vol. 35, no. 12, December 2000, pp. 1934-1941	
MBS	AO	STEYAERT et al., "A 2-V CMOS Cellular Transceiver Front-End", IEEE Journal of Solid-State Circuits, vol. 35, no. 12, December 2000, pp. 195-1907	

Examiner Signature	SHINGLETON	Date Considered	May 1, 2003
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